

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hyoung-Joon Kim, Young-Wook Park, Byeong-Yun Nam

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: **METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES  
PROVIDING IMPROVED SHORT PREVENTION**

Date: November 12, 2003

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on form PTO-1449. Items 1-2 listed on the PTO-1449 were cited in parent application Serial No. 10/052,721, filed January 18, 2002. Since the benefit of this application is claimed under 35 U.S.C. §120, no copies need to be furnished in accordance with 37 C.F.R. §1.98(d); however, copies will be furnished on request. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,



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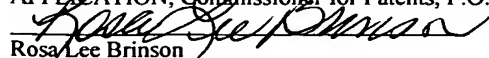
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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated above and is addressed to: Mail Stop PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Rosa Lee Brinson

